

62763



MAY 82/REV 0
EK-MSVPL-CG-001

MSV11-PL Memory Module Configuration Sheet

The MSV11-PL is a quad height memory option for LSI-11 based systems.
The MSV11-PL features:

- Parity generation and checking
- Support of a 22-bit addressing environment
- A half megabyte on one quad height module
- Single 5 V supply
- On-board memory refresh.

Power Jumpers		
Jumper	Nonbattery Backup*	Battery Backup
W3	R	R
W4	I	I
W5	I	I
W9	I	R
W10	R	R
W11	R	R
W12	R	I
W13	I	R
W14	R	I
W15	I	R

I = Installed, R = Removed

*Factory configuration

CONFIGURING STARTING ADDRESS

Starting Address Range		Jumpers to Ground (Pin Y)		
Decimal K Bytes	Octal K Bytes	Pin X (A21)	Pin W (A20)	Pin V (A19)
000-496*	0000 0000-0174 0000	R	R	R
512-1008	0200 0000-0374 0000	R	R	I
1024-1520	0400 0000-0574 0000	R	I	R
1536-2032	0600 0000-0774 0000	R	I	I
2048-2544	1000 0000-1174 0000	I	R	R
2560-3056	1200 0000-1374 0000	I	R	I
3072-3568	1400 0000-1574 0000	I	I	R
3584-4080	1600 0000-1774 0000	I	I	I

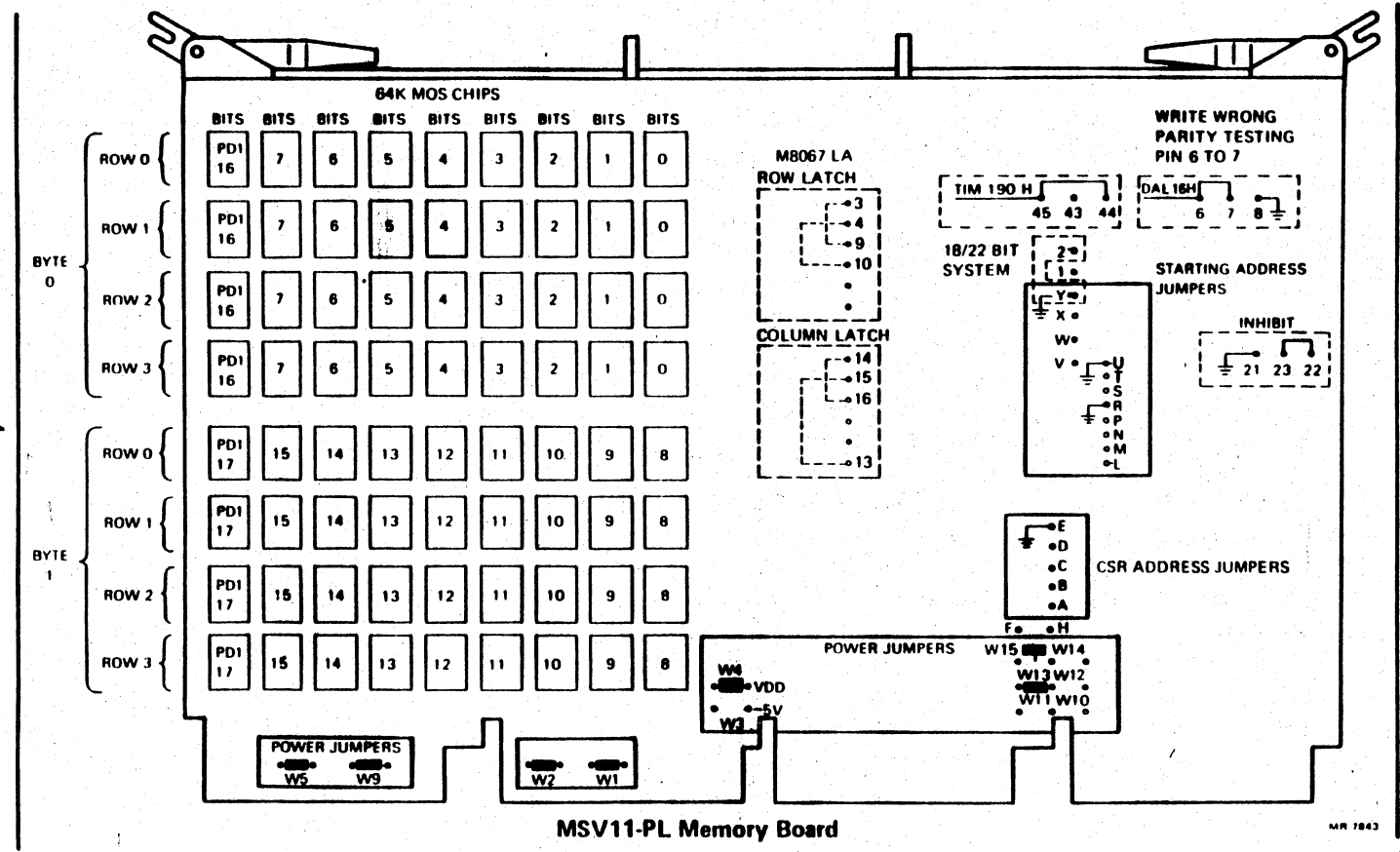
I = Installed, R = Removed

*Factory configuration

Partial Starting Address within Selected Range

Bytes	Octal K Bytes	Jumpers to Ground (Pin R)				
		Pin P (A18)	Pin N (A17)	Pin M (A16)	Pin L (A15)	Pin T (A14)
	0000 0000	R	R	R	R	R
	0004 0000	R	R	R	R	I
	0010 0000	R	R	R	I	R
	0014 0000	R	R	R	I	I
	0020 0000	R	R	I	R	R
	0024 0000	R	R	I	R	I
	0030 0000	R	R	I	I	R
	0034 0000	R	R	I	I	I
	0040 0000	R	I	R	R	R
	0044 0000	R	I	R	R	I
	0050 0000	R	I	R	I	R
	0054 0000	R	I	R	I	I
	0060 0000	R	I	I	R	R
	0064 0000	R	I	I	R	I
	0070 0000	R	I	I	I	R
	0074 0000	R	I	I	I	I
	0100 0000	I	R	R	R	R
	0104 0000	I	R	R	R	I
	0110 0000	I	R	R	I	R
	0114 0000	I	R	R	I	I
	0120 0000	I	R	I	R	R
	0124 0000	I	R	I	R	I
	0130 0000	I	R	I	I	R
	0134 0000	I	R	I	I	I
	0140 0000	I	I	R	R	R
	0144 0000	I	I	R	R	I
	0150 0000	I	I	R	I	R
	0154 0000	I	I	R	I	I
	0160 0000	I	I	I	R	R
	0164 0000	I	I	I	R	I
	0170 0000	I	I	I	I	R
	0174 0000	I	I	I	I	I

I, R = Removed
configuration



MSV11-PL Memory Board

MR 7843

CSR Address Selection

Module Number	22-bit LSI-11 Bus Address	18-bit LSI-11 Bus Address	Jumpers to Ground (Pin E)			
			D	C	B	A
1*	1777 2100	772100	R	R	R	R
2	1777 2102	772102	R	R	R	I
3	1777 2104	772104	R	R	I	R
4	1777 2106	772106	R	R	I	I
5	1777 2110	772110	R	I	R	R
6	1777 2112	772112	R	I	R	I
7	1777 2114	772114	R	I	I	R
8	1777 2116	772116	R	I	I	I
9	1777 2120	772120	I	R	R	R
10	1777 2122	772122	I	R	R	I
11	1777 2124	772124	I	R	I	R
12	1777 2126	772126	I	R	I	I
13	1777 2130	772130	I	I	R	R
14	1777 2132	772132	I	I	R	I
15	1777 2134	772134	I	I	I	R
16	1777 2136	772136	I	I	I	I

I = Installed, R = Removed

*Factory configuration

Bus Grant Continuity Jumpers

Backplane	Machine Type	W1	W2
H9270 (4 Slot backplane)	Q/Q	In	In
H9275* (9 Slot backplane)	Q22/Q22	In	In
H9273 (4 Slot backplane)	Q/CD	Out	Out
H9276 (9 Slot backplane)	Q22/CD	Out	Out

*Factory configuration

General Jumpers

Pin Number	Function
6 to 7	Installed* — Enable write wrong parity
8 to 7	Installed — Disable write wrong parity
2 to Y	Installed — 18-bit machine
	Removed* — 22-bit machine
43 to 44	Installed — Single voltage MOS RAM access time (150 ns device)
45 to 44	Installed* — Multiple voltage MOS RAM access time (200 ns device)
21 to 23	Not used
22 to 23	Not used
F to H	Installed — Force starting address to 16K
	Removed* — Disables force function
3 to 9	Installed* — Connected on 16K and 64K MOS chip
13 to 15	Installed* — Connected on 16K and 64K MOS chip
4 to 10	Installed* — Connected only on 64K MOS chip
14 to 16	Installed* — Connected only on 64K MOS chip

*Factory configuration